

AMENDMENTS TO THE SPECIFICATION:

The present Amendment has been prepared in accordance with a revised format established by the U.S. Patent and Trademark Office, as permitted in the Pre-OG Notice entitled "Amendments in a Revised Format Now Permitted."

Please amend the title of the invention as follows:

A
--COLOR FILTER ARRAY AND IMAGE PICKUP APPARATUS
HAVING A COLOR FILTER ARRAY--

Please amend the paragraph beginning at page 17, line 14 of the specification as follows:

PP/Cnd
Referring to Fig. 5, reference numeral 1 represents a vertical scan circuit for generating an enable signal which enables a control signal of each row, the enable signal sequentially becoming active in the vertical direction. Reference numeral 100 represents a photodiode serving as a photodetector for converting incidence light into electric charges. Reference numeral 101 represents a transfer transistor for transferring the electric charges generated by the photodiode 100 to a floating diffusion region ~~101~~ 102 which temporarily stores the transferred electric charges. Reference numeral 103 represents a reset transistor for discharging electric charges accumulated in the gate of an amplifier transistor 104. Reference numeral 121 represents a switching transistor. The elements 100 to 104, and 121 constitute one pixel. Reference

*for
cont*

numeral 112 represents a constant current source transistor which is activated by a voltage applied to a terminal 7. Reference numeral 105 represents a transistor for discharging electric charges in capacitors 109, 110, 117, and 118. Reference numeral 106 represents a current distribution division transistor for connecting the source of the transistor 104 to the capacitor 109. Reference numeral 107 represents a current distribution transistor for connecting the source of the transistor 104 to the capacitor 110. The capacitors 109 and 110 function as a line memory which is charged by a voltage supplied from the transistor 104. Reference numeral 108 represents an averaging transistor for averaging the electric charges in the capacitors 109 and 110. Reference numeral 111 represents a switching transistor for applying a voltage of the line memory 109 to a buffer 123 at the front stage of a differential amplifier 122 which amplifies a difference between voltages across the capacitors 109 and 109'. Reference numeral 113 represents a switching transistor for connecting the source of the transistor 104 to a capacitor 117. Reference numeral 114 represents a switching transistor for connecting the source of the transistor 104 to a capacitor 118. The capacitors 117 and 118 are charged by a current supplied from the source of the transistor 104. Reference numeral 115 represents a switching transistor for controlling to average the electric charges stored in the capacitors 117 and 117'. Reference numeral 116 represents a switching transistor for controlling to

average the electric charges stored in the capacitors 118 and 118'.

Reference numeral 119 represents a switching transistor for supplying a voltage of the line memory 117 to a buffer 129 at the front stage of a differential amplifier 127 which amplifies a difference between voltages across the capacitors 117 and 118'. The constant current source transistor 112 is activated in the unit of a row, and paired with the transistor 104 to constitute an amplifier.

*AS
Conf*